



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**Response Under 37 C.F.R. § 1.116 Expedited Procedure**

In re application of: McKenney

Art Unit: 2151

Application No. 09/127,085

Filed: July 31, 1998

For: HIGH SPEED METHOD FOR  
MAINTAINING A SUMMARY OF  
THREAD ACTIVITY FOR  
MULTIPROCESSOR COMPUTER  
SYSTEMS

Examiner: Majid A. Banankhah

Date: September 4, 2001

CERTIFICATE OF MAILING

I hereby certify that this paper and the documents referred to as being attached or enclosed herewith are being deposited with the United States Postal Service on September 4, 2001 as First Class Mail in an envelope addressed to: BOX NO FEE COMMISSIONER FOR PATENTS, WASHINGTON, D.C. 20231.

*[Signature]*  
Attorney for Applicant

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AMENDMENT AFTER FINAL REJECTION

In response to the Office Action mailed June 4, 2001, paper number 5, Applicant submits the following amendments and remarks:

In the Claims:

Please amend the claims as follows (A marked-up version is attached hereto):

2. (Amended) In a multiprocessor computer system having multiple interconnected processing nodes each with one or more processors and physical memory, a data structure stored in the physical memory on each node for storing a number of the current generation of data elements being processed by a processor on a node, the data structure comprising a variable stored in the physical memory of each node and containing the current generation number, wherein the current generation number on the nodes are updated in lockstep so that the nodes have local access to copies of the current generation number.

3. (Amended) In a multiprocessor computer system having multiple interconnected processing nodes each with one or more processors and physical memory, a method for a

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